Customer No. 22,852 Attorney Docket No.: 04329,3117

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	
Yoshihiro NISHIDA) Group Art Unit: Not Yet Assigned
Serial No.: Not Yet Assigned) Examiner: Not Yet Assigned
Filed: August 19, 2003)
For: COMPUTER SYSTEM AND MEMORY CONTROL METHOD)))

MAIL STOP PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicant brings to the Examiner's attention the document listed on attached Form PTO-1449. A copy of the listed document is attached. Applicant respectfully requests that the Examiner consider the document listed on attached Form PTO-1449 and indicate that it was considered by making an appropriate notation on this form. This Information Disclosure Statement is being filed with the above-referenced application.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed document is material or constitutes "prior art." If the Examiner applies the document as prior art against any claim in the application and applicant determines that the cited document does not constitute "prior art" under United States law, applicant reserves the right to

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present to the office the relevant facts and law regarding the appropriate status of such document. Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed document, should it be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,

GARRETT & DIONNER, L.L.P.

Dated: August 19, 2003

Richard V/Burgujian

Reg. No. 31,744

Enclosures RVB/FPD/sci

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INFORMATION DISCLOSURE CITATION

Atty. Docket No.	04329.311	Serial No.	Not Yet Assigned	
Applicant	Yoshihiro NISHIDA			
Filing Date	August 19, 2003	Group:	Not Yet Assigned	

	U.S. PATENT DOCUMENTS					
Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate

	FOREIGN PATE	NT DOCUMENT	S		· · · · · · · · · · · · · · · · · · ·
Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
			<u> </u>		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)				
	IDT,, "High-Speed 4K x 16 Dual-Port Static RAM", IDT7024S/L; FIFO Memory, pp. 1-21, Integrated Device Technology, Inc., Santa Clara, California, (April 2000).			

Examiner	aminer Date Considered	
*Examiner: Initial if reference considered, whether o through citation if not in conformance an communication to applicant.		ered, whether or not citation is in conformance with MPEP 609; draw line conformance and not considered. Include copy of this form with next ant.
Form PTO 14	149	Patent and Trademark Office - U.S. Department of Commerce